

A+ 1101-1102 Notes

Personal notes for the All-in-One exam guide (ISBN 9781264610150)

I will probably be skipping a lot of things here that is prior knowledge to me

- There is a distinction between CMOS and Flash ROM (SPI flash), with the latter being more modern
- PCIe is considered a serial bus, even though it usually involves multiple bonded lanes
 - Uses a pair of differential signals per lane
- Multimode fiber up to 600 ft

Book errors

Annoyingly, the exam guide has typos..

- 732 Q6
- 649 Q8
 - ps is non-interactive
- 767 Q2
 - incorrect letter answer
- 749
 - fiber is half duplex, but it does not mean you need two fibers for all connections...
example: residential fiber using SC
- 1243 Q5
 - Answer should be change documentation

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